

## GENERAL DESCRIPTION

The BM9164 family of a linear voltage linear regulators developed utilizing BM unique BiCMOS technology featured low quiescent current (50uA), low dropout voltage, high output voltage accuracy. The space-efficient SOT223, TO252 package is attractive for "Pocket" and "Hand Held" applications.

Output voltages are set at the factory and trimmed to 1.5% accuracy. Voltages from 3.3V to 5.0V are available.

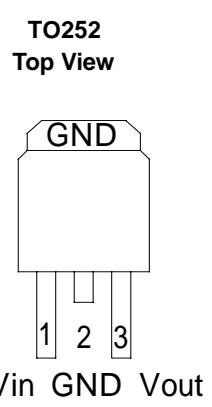
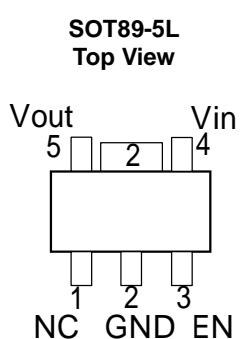
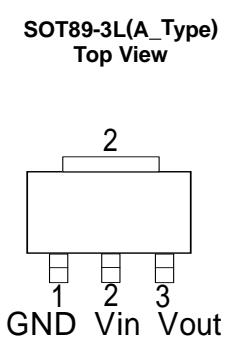
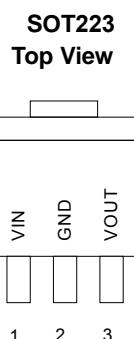
These rugged devices have both Thermal Shutdown, and Current Fold-back to prevent device failure under the "Worst" of operating conditions.

The BM9164 is stable with an output capacitance of 10 $\mu$ F or greater.

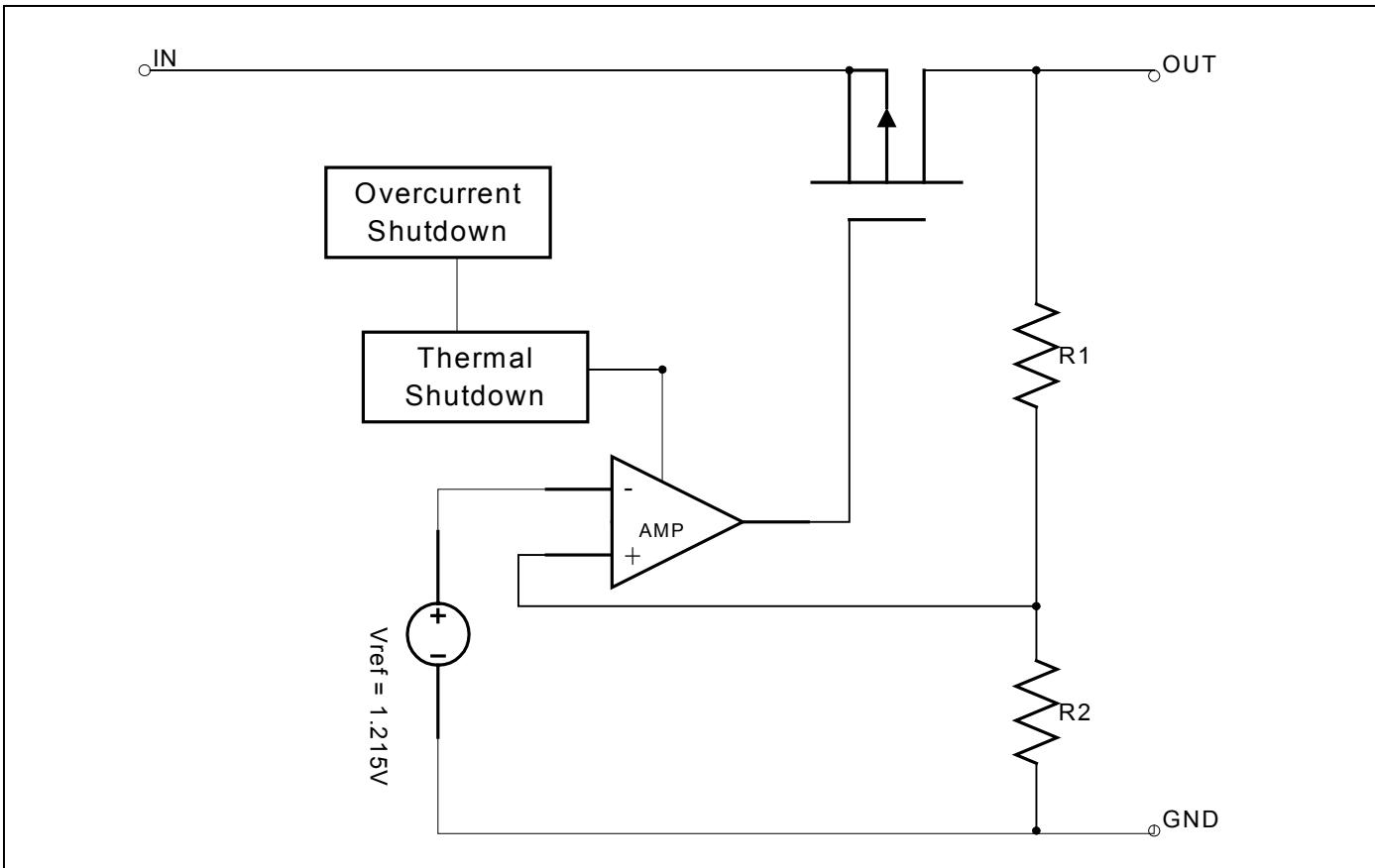
## APPLICATIONS

- ◆ Battery-powered devices
- ◆ Personal communication devices
- ◆ Home electric/electronic appliances
- ◆ PC peripherals , hard-disk

## PIN CONFIGURATION



两款SOT89的静态电流只有1uA，因封装小，耐压可到15V，输出电流0.7A

**BLOCK DIAGRAM****ORDERING INFORMATION**

Part Number	Output Voltage	Temperature Range	Package
BM9164-5.0	5.0V	-20°C ~ +85	SOT223
BM9164-3.3	3.3V	-20°C ~ +85	SOT-223
BM9164-5.0	5.0V	-20°C ~ +85°C	TO252

**ABSOLUTE MAXIMUM RATINGS**

Input Voltage .....	+18V
Output Current .....	1.2A
Output Voltage .....	GND-0.3V to $V_{IN}+0.3V$
ESD Classification .....	B

**OPERATING RATINGS**

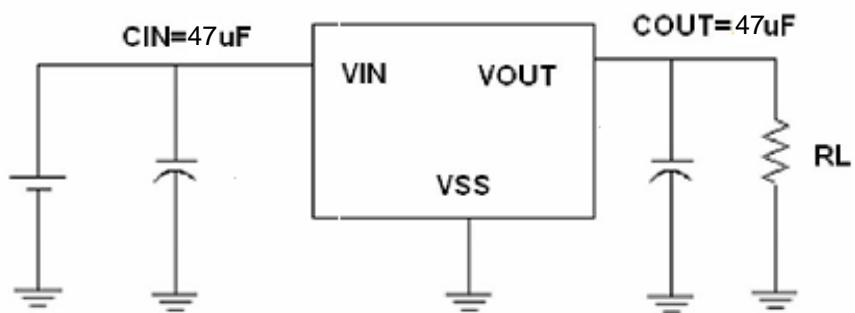
Supply Voltage .....	+2V to +16V
Ambient Temperature Range ( $T_A$ ) .....	-40°C to +85°C
Junction Temperature Range .....	-40°C to +125°C

**THERMAL INFORMATION**

Parameter		Maximum	Unit
Thermal Resistance ( $\Theta_{jc}$ )	SOT-223	160	°C/W
Internal Power Dissipation ( $P_D$ ) ( $\Delta T = 100^\circ\text{C}$ )	SOT-223	625	mW
Maximum Junction Temperature		150	°C
Maximum Lead Temperature (10 Sec)		300	°C

\*With Junction sink capable of twice times of  $\Theta_{jc}$

Caution: Stress above the listed absolute rating may cause permanent damage to the device.

**Typical Application Circuit**

## ELECTRICAL CHARACTERISTICS

 $T_A = +25^\circ\text{C}$ ; unless otherwise noted

Parameter	Symbol	Test Conditions	BM9164			Unit
			Min.	Typ.	Max.	
Input Voltage	$V_{IN}$		+2		+18	V
Output Voltage Accuracy	$V_{OUT}$	$I_O = 1\text{mA to } 1.5\text{A}$	-1.5		1.5	%
Dropout Voltage	$V_{DROPOUT}$	$V_{OUT} = V_{O(NOM)} - 4\%$ ,	$I_{OUT} = 0.8\text{A}$		400	
			$I_{OUT} = 600\text{mA}$		300	mV
			$I_{OUT} = 100\text{mA}$		200	mV
Output Current	$I_O$	$V_{OUT} > 1.5\text{V}$		800		mA
Current Limit	$I_{LIM}$	$V_{OUT} > 1.5\text{V}$		1200		mA
Short Circuit Current	$I_{SC}$	$V_{OUT} < 0.4\text{V}$		750	1200	mA
Quiescent Current	$I_Q$	$I_O = 0\text{mA}$		50		uA
Ground Pin Current	$I_{GND}$	$I_O = 1\text{mA to } 1\text{A}$		50		uA
Line Regulation	$REG_{LINE}$	$I_{OUT} = 5\text{mA}$	$V_{OUT} \leq 4.0\text{V}$		1.5	%
			$V_{OUT} > 4.0\text{V}$	2		%
Load Regulation	$REG_{LOAD}$	$I_O = 0 \sim 0.8\text{A}$		0.2	1.5	%
Over Temperature Shutdown	OTS			150		°C
Over Temperature Hysteresis	OTH			30		°C
$V_{OUT}$ Temperature Coefficient	TC			30		ppm/°C
Power Supply Reject	PSRR	$I_O = 100\text{mA}$ $C_O = 22\mu\text{F ceramic}$	$f = 1\text{kHz}$	72		
			$f = 10\text{kHz}$	60		
			$f = 100\text{kHz}$	45		
Output Voltage Noise	eN	$f = 10\text{Hz to } 100\text{kHz}$ $I_O = 10\text{mA}$	$C_O = 22\mu\text{F}$	30		
			$C_O = 47\mu\text{F}$	20		$\mu\text{Vrms}$

Note 1.  $V_{IN(MIN)} = V_{OUT} + V_{DROPOUT}$

## DETAILED DESCRIPTION

The BM9164 family of BiCMOS regulators contain a pass transistor, voltage reference, error amplifier, over-current protection, thermal shutdown, and short circuit protection.

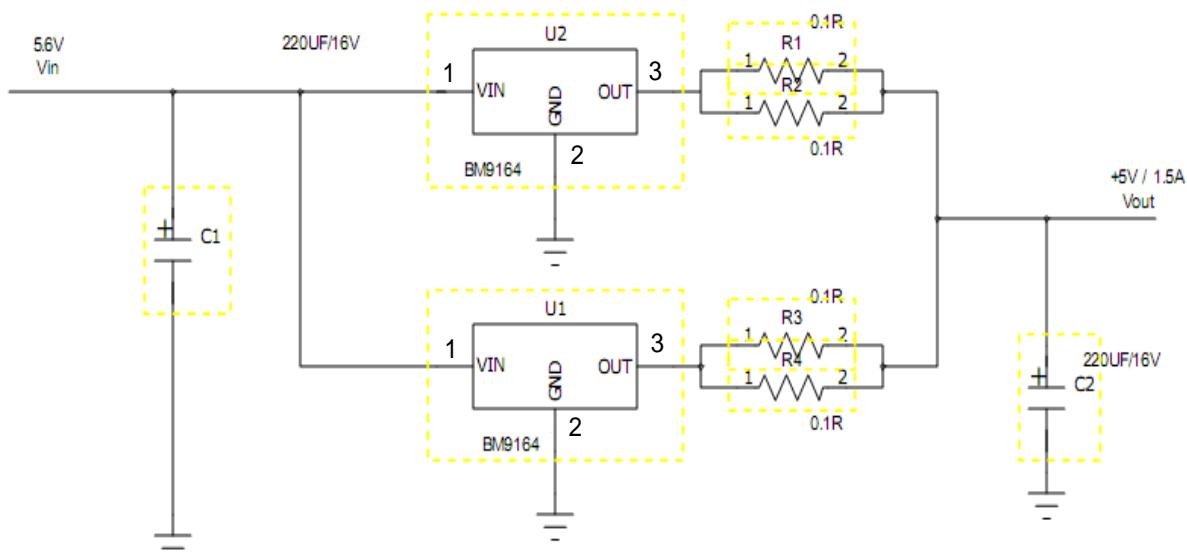
The P-channel pass transistor receives data from the error amplifier, over-current shutdown, short output protection, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150°C, or the current exceeds 1.2A. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120°C.

The BM9164 behaves like a current source when the load reaches 1.2A. However, if the load impedance drops below 0.3Ω, the current drops back to 600mA to prevent excessive power dissipation. Normal operation is restored when the load resistance exceeds 0.75Ω.

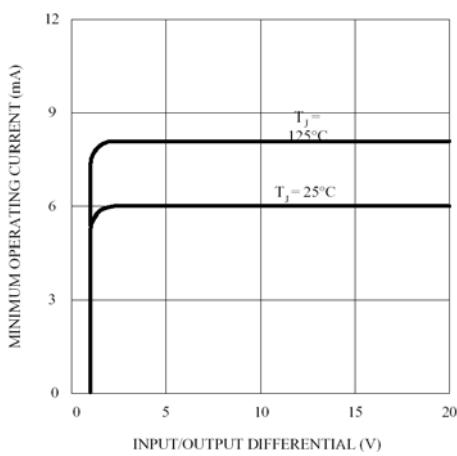
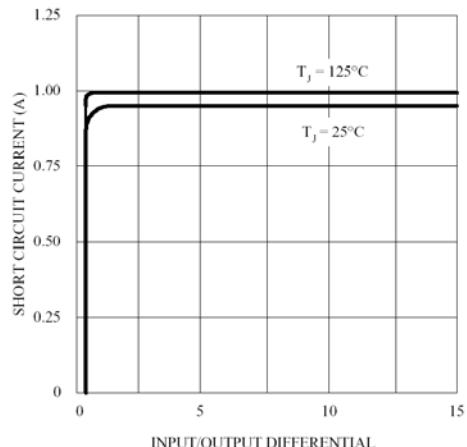
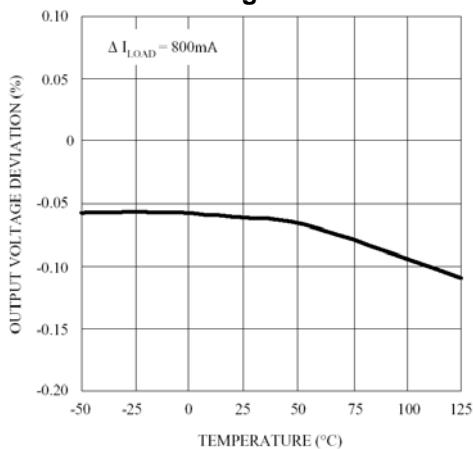
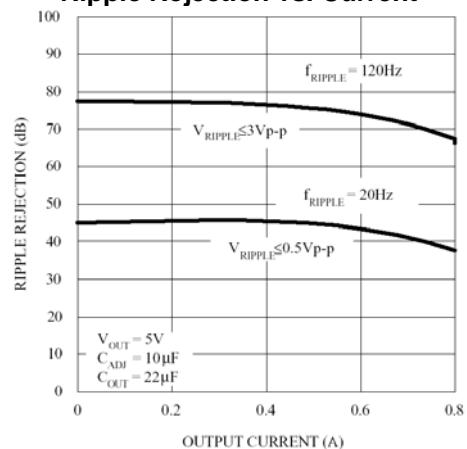
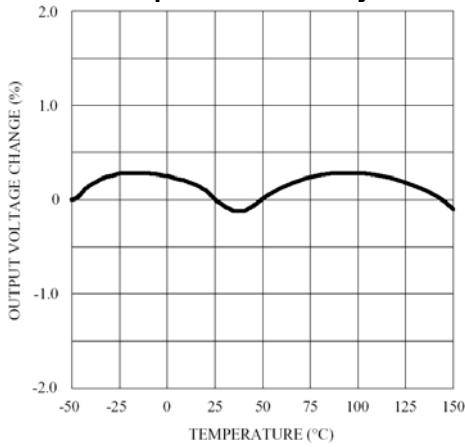
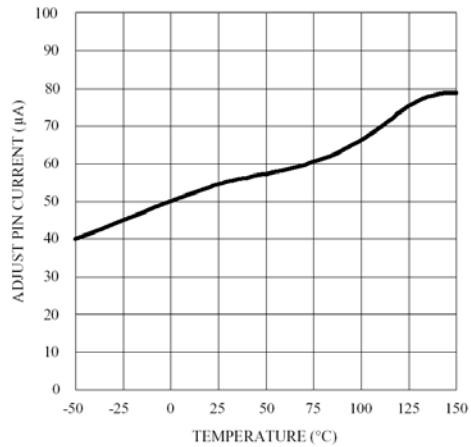
## EXTERNAL CAPACITOR

The BM9164 is stable with an output capacitor to ground of 22μF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1μF ceramic capacitor with a 22μF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost.

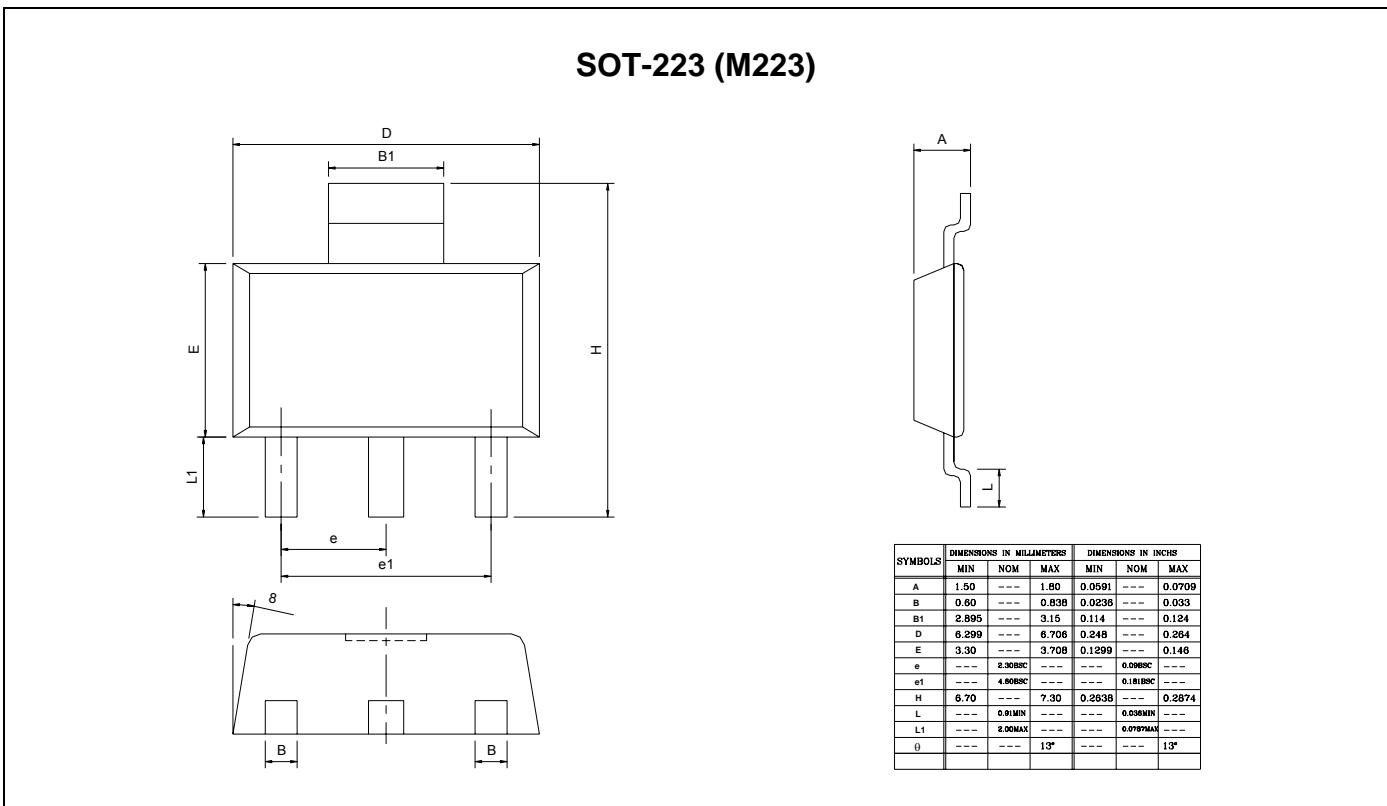
A second capacitor is recommended between the input and ground to stabilize VIN. The input capacitor should be larger than 22μF to have a beneficial effect. All capacitors should be placed in close proximity to the pins. A “quiet” ground termination is desirable.



锂电池降压式大电流充电典型电路

**TYPICAL ELECTRICAL CHARACTERISTICS****Minimum Operating Current****Short-Circuit Current****Load Regulation****Ripple Rejection vs. Current****Temperature Stability****GND Pin Current**

## PACKAGE DIMENSION

**TO252-2 (DPAK)**